

FIGURE 2

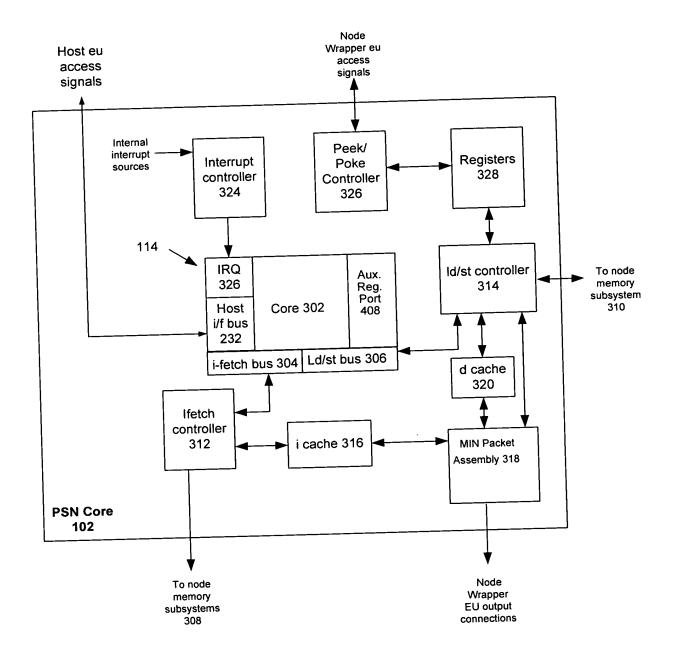


Figure 3

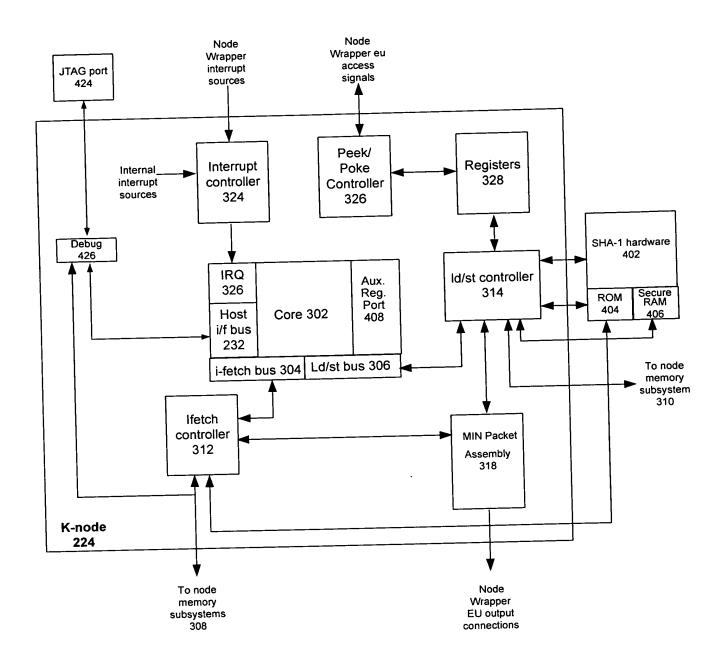


Figure 4

Data Memory Map	Instruction Memory Map
	0x000 0000 0x0000 000
Made Momery	0x0000 0000 Node Memory
Node Memory	0,,000 4000
Reserved	0x0000 4000 Reserved 0x000 8000
	0x0000 8000 ROM
ROM	0x0000 A0000 0x000 A0000
Reserved	Reserved 0x200 0000
	External Instruction
Secure Ram	Memory Overlay 0x0000 CAC0
Reserved	0x0000 CACU : 0x01ff fff
	0x0001 8000
Node Registers	0x0001 9000 Note: External Instruction Memory Overaly and
Reserved	Note: External Instruction Memory Overlay and External Data Memory have the same memory locations within external memory
input/Output Buffer	memory locations within external memory
Access	0x0060 0000
Forward/Backward	0x0000 0000
Acknowledgement	0x0070 0000
Own Node Wrapper	(**********************************
Access	0x0070 4000
Reserved	0x0200 0000 : 0x0000 0000 (K-Node addr : XMC addr)
External Instruction	
Memory Overlay	0x0400 0000 : 0x0200 0000 (K-Node addr : XMC addr)
Reserved	0x1000 0000 : 0x0000 0000 (K-Node addr : XMC addr)
External Data Memory	
External Data Memory	0x2000 0000 : 0x1000 0000 (K-Node addr : XMC addr)
Reserved	0x4000 0000 : 0x0000 0000 (K-Node addr : XMC addr)
Chip 0	
External Data Memory	0x5000 0000 : 0x0000 0000 (K-Node addr : XMC addr)
Chip1	
External Data Memory	0x6000 0000 : 0x0000 0000 (K-Node addr : XMC addr)
Chip 2	
External Data Memory	0x7000 0000 : 0x0000 0000 (K-Node addr : XMC addr)
Chip 3	
External Data Memory	0x8000 0000 : 0x1000 0000 (K-Node addr : XMC addr)
ACM Chip	
and Node Access	·
(Details in Figure 2)	
	0x8340 0000
Reserved	- 0,00,70 0000

Figure 5A

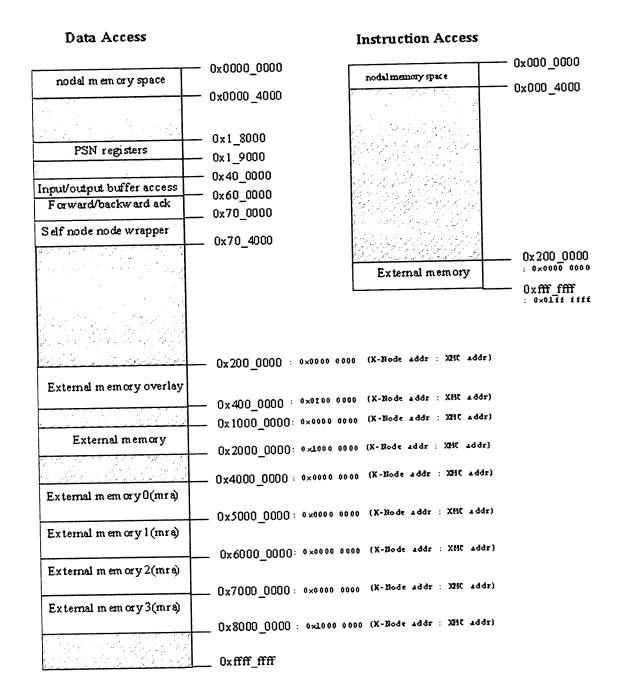


Figure 5B

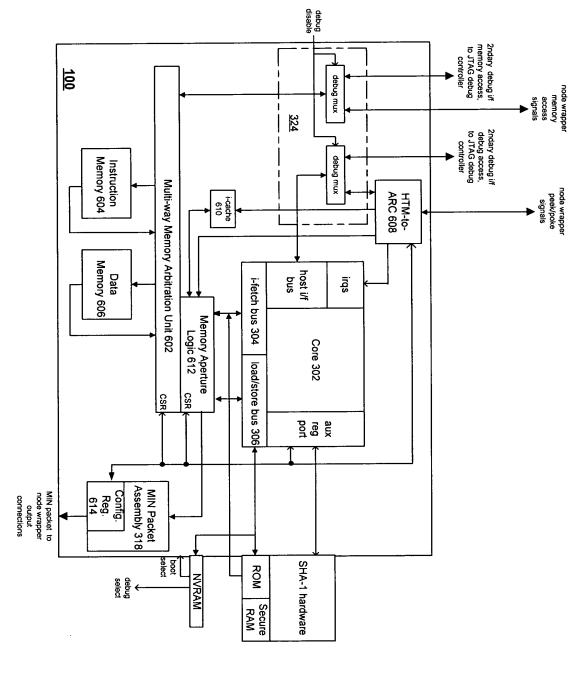


Figure 6

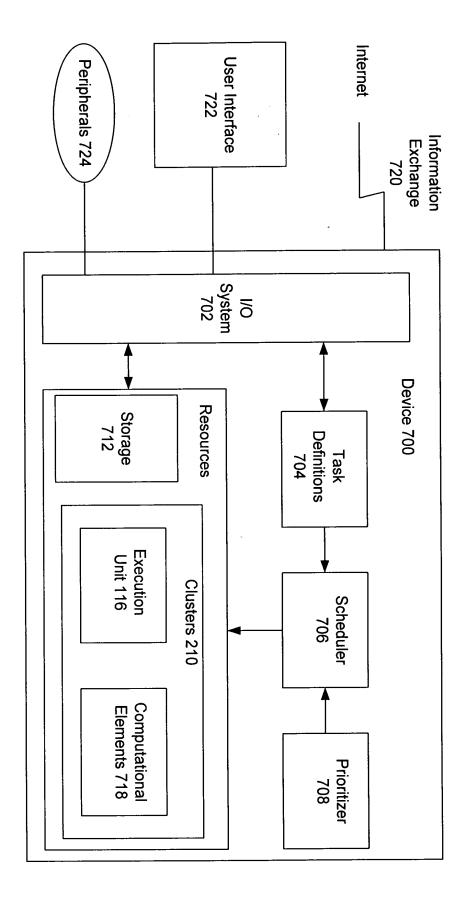


Figure 7